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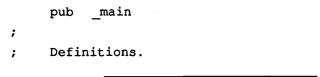
Transputer Education Kit User Guide Errata

15 Oct 1990

Please make the following corrections to the Transputer Education Kit User Guide:

Page16

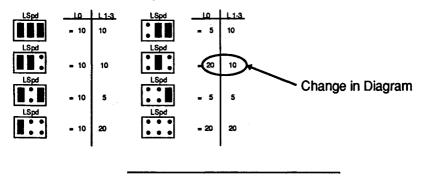
The program listed in section 2.5.3 is missing the first two lines. The following shows the proper placement of the first few lines of the listing:



Page 29

The following graphic in section 3.4.2 is in error. With one jumper installed, the user can select 20 Mbits/s for Link0 and 10 Mbits/s for Links 1,2 &3.

This is the corrected image:



Page 47

The schematic for the Eight Channel A to D Converter has an error that can destroy the chip. On the ADC 0808, VRef+ should come out on pin 12 and VRef- should come out on pin 16. The diagram on page 47 shows these two pin numbers reversed. Note also that Vcc for the ADC 0808 should be connected to pin 11 and Gnd to pin 13.

Page 53

The diagram showing the PC Link Address jumper block is incorrect. The corrected diagram is shown below:

	PC Link Address												
•	٠	٠	٠	•	٠	•			•			٠	
•	٠	٠	۲	٠			٠	•	•	٠	•	•	
SS	- 0,	Ě	3,2	SS	5,4	Ę	7,6	. SS	3,2	B	0.1	Sod	
-			~						-	Y	_		

Pages 67-69

The syntax on several lines in Programs 1, 2 and 3 is incorrect. The corrected lines are shown below: (The corrected versions of these programs are available in source form on the "Examples" diskette.)

Program 1

Line 21 ChanOutChar(from echo, ch); /*Send it to echo*/ Program 2 Line 5 int echo(Process *p, Channel *in, Channel *out); Line 10 Channel *to echo, *from echo; Line 29 ChanOutChar(from echo, ch); /*Send it to echo*/ Program 3 Line 14 to echo = &te; /*Initialize channel pointers*/ Line 15 from echo = &fe: /*to address of te and fe*/ Line 25 ChanOutChar(from echo, ch); /*Send it to echo*/

If you find additional errors, please let us know. Please forward your comments to:

Transputer Education Kit -- Errata Computer System Architects 950 N. University Avenue Provo, Utah USA 84604-3422