Read Me First (UGR0105-000, REVA)

INITIALIZE CONTROLLER Information

The following information replaces the information in the section *Bit 1 SCSI Bus Reset Interrupt* on page 5-47 of your *V/SCSI-2 4220 Cougar II and 4220 Cougar Wide Users Guide:*

Setting this bit causes the board to interrupt the host if a reset occurs on the SCSI bus. SCSI reset interrupts are generated when a SCSI reset is detected. The firmware returns the ID of the bus where the reset occurred in the unit address field of a pass-through IOPB structure (word 0xf). The firmware also sets the SCSI reset bit in the Command Response Status Word (CRSW) in the returned CRB.

If there were commands active on the SCSI bus at the time of the reset, the firmware returns them to the adapter driver first (with error 0x15). The firmware then issues the SCSI reset interrupt.